

**ELECTRÓNICA
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- 3 **Peña-Pérez, A.; Tlelo-Cuautle, E.; Díaz-Méndez, A.;** Sánchez-López, C., "Design of a CMOS Compatible CFOA and its Application in Analog Filtering", Revista IEEE Latinoamerica, Vol. 5, Issue 2, May 2007. ISSN: 1548-0992, Revista Electrónica.
- 4 **Muñiz- Montero, A. Díaz Sánchez,** R. González-Carvajal and J.M. Rocha-Pérez, "New Strategies to Improve Offset and the Speed-Accuracy-Power Tradeoff in CMOS Amplifiers," Analog Integrated Circuits and Signal Processing: An International Journal, Volume 53, Numbers 2-3, December 2007, pp. 81-95.
- 5 **P.Halevi, Adán S.Sánchez, and E. Galindo-Linares** "Tuning and switching of the spontaneous emission in one-dimensional photonic crystals Optics Communications", 269, 351-355 (2007).
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- 10 **M. Moreno, A.Kosarev, A.Torres**, R.Ambrosio. "Study of a Fabrication Process and Characterization of One-Dimensional Array of Un-cooled Microbolometers Based on Germanium Films Deposited by Plasma", Mater. Research Symposium Proceedings, 2007 v.989, p.p. 0989-A19-01 – 06.
- 11 **M. Moreno, A. Kosarev, A. Torres**, "Fabrication and characterization of un-cooled micro-bolometers based on germanium films obtained low frequency plasma deposition", Revista Mexicana de Física, Vol. 53, No. 7, pp 327-**330**, 2007.
- 12 Morales, C. Domínguez, J. Barreto, M. Riera **M. Aceves, J. A. Luna, Z. Yu, R. Kiebach**. "Spectroscopical Analysis of Luminescence Silicon Rich Oxide Films" Revista Mexicana de física S 53 (7) 279-282 (2007).
- 13 **Esteban Tlelo-Cuautle, Jesús-Manuel Muñoz-Pacheco**, "Numerical simulation of Chua's circuit oriented to circuit synthesis", International Journal of Nonlinear Sciences and Numerical Simulation, vol. 8, no. 2, pp. 249-256, ISSN: 1565-1339, 2007.
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- 16 **E. Tlelo-Cuautle, M. A. Duarte-Villaseñor, J. M. García-Ortega**, C. Sánchez-López, "Designing SRCOs by combining SPICE and Verilog-A", International Journal of Electronics, vol. 94, no. 4, pp. 373-379, ISSN: 0020-7217, April 2007. DOI: 10.1080/00207210701294940.
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- 1 **A. Peña Pérez, Esteban Tlelo Cuautle and Alejandro Díaz Méndez,**
Diseño de un CFOA Compatible en Tecnología CMOS y su Aplicación en
Circuitos Lineales y No Lineales, Científica, vol. 11, no. 003, pp 121-127,
julio-sep 2007.

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